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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/692,586

10/23/2003

Ram Voorakaranam

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7590

10/20/2004

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EXAMINER

BHAT, ADITYA S

ART UNIT

PAPER NUMBER

2863

DATE MAILED: 10/20/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

10/692,586

Applicant(s)

VOORAKARANAM ET AL.

Examiner

Aditya S Bhat

Art Unit

2863

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 23 October 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1, 11 and 15-19 is/are rejected.
- 7) ☒ Claim(s) 2-10 and 12-14 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 23 October 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 5/17/04.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

## **DETAILED ACTION**

### ***Claim Objections***

Claims 2-10 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 6, 10, and 12-14 are objected to because of the following informalities: the alternate and signature tests do not appear to be properly defined in the specification. The word rest in claim 14 should be retested. Appropriate correction is required.

### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1, 11, and 15-19 rejected under 35 U.S.C. 103(a) as being obvious over Chatterjee et al. (USPN 6,625,785)

The applied reference has a common assignee with the instant application. Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art only under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 103(a) might be overcome by: (1) a showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not an invention "by another"; (2) a showing of a date of invention for the claimed subject

matter of the application which corresponds to subject matter disclosed but not claimed in the reference, prior to the effective U.S. filing date of the reference under 37 CFR 1.131; or (3) an oath or declaration under 37 CFR 1.130 stating that the application and reference are currently owned by the same party and that the inventor named in the application is the prior inventor under 35 U.S.C. 104, together with a terminal disclaimer in accordance with 37 CFR 1.321(c). For applications filed on or after November 29, 1999, this rejection might also be overcome by showing that the subject matter of the reference and the claimed invention were, at the time the invention was made, owned by the same person or subject to an obligation of assignment to the same person. See MPEP § 706.02(I)(1) and § 706.02(I)(2).

With regards to claim 1, Chatterjee et al. (USPN 6,625,785) teaches a method for using an alternate performance test to test products with at most substantially the same margin of error as a specification test, comprising:

establishing a specification test limit within which a product would be accepted under specification test criteria and inner and outer alternate test error bounds relative to the specification test limit; (Col. 5, lines 19-28)

initially testing the product with the alternate test; (Col. 11, lines 15-20)

accepting the product if the alternate test result is within the inner alternate test error bound; (Col. 2, lines 17-25)

rejecting the product if the alternate test result is outside the outer alternate test error bound; and (Col. 2, lines 17-25)

retesting the product using the specification test if the alternate test result is on or between the alternate error bounds.

Chatterjee et al. (USPN 6,625,785) discloses the claimed invention except for retesting the product using specification test if the alternate test result is neither in the inner bounds or out of the outer bounds . It would have been obvious to one having ordinary skill in the art at the time the invention was made to retest the IC, since it has been held that discovering an optimum value of a result effective variable involves only routine skill in the art. In re Boesch, 617 F.2d 272, 205 USPQ 215 (CCPA 1980).

With regards to claim 11 Chatterjee et al. (USPN 6,625,785) teaches the alternate test provides a reduction of test time from that required by the specification test (Col. 11, lines 19-20)

With regards to claim 15-16, Chatterjee et al. (USPN 6,625,785) teaches incorporated into an automated product test system. (Col.5, lines 25-26)

With regards to claim 17, Chatterjee et al. (USPN 6,625,785) teaches the standard test comprises a set of individual specification tests for product parameters, (Col.2, lines 18-20) and the alternate test (Col.11, lines 15-20) comprises a set of individual performance tests from which said parameters may be extracted.

With regards to claim 18, Chatterjee et al. (USPN 6,625,785) teaches the products tested are electronic integrated circuits. (See figure 3)

With regards to claim 19, Chatterjee et al. (USPN 6,625,785) teaches the products tested are electronic systems on a chip. (Col. 1, line 61)

**Conclusion**

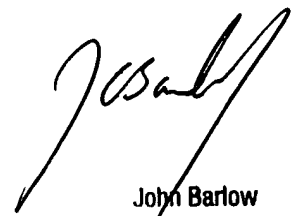
The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Ohmhart et al (USPN 5,589,765) teaches a method for final testing of semiconductor devices, and Vancura (USPUB 2004/0044935) teaches a method and apparatus for improved integrated circuit memory testing,.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Aditya S Bhat whose telephone number is 571-272-2270. The examiner can normally be reached on M-F 9-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Barlow can be reached on 571-272-2269. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Aditya Bhat  
October 15, 2004



John Barlow  
Supervisory Patent Examiner  
Technology Center 2800